



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar

Examiner: Nicole R Blan

Patent No.: 7,645,344

Group Art Unit: 1792

Issue Date: January 12, 2010

Docket No: 2003-0542.00/US

Title: METHOD OF CLEANING SEMICONDUCTOR SURFACES

Commissioner for Patents  
Office of Patent Publication  
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Respectfully submitted,

Date: 02/12/2010 By: \_\_\_\_\_

Russ Slifer  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 12 day of February 2010.

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